

# Solving XCSP3 Constraint Problems Using Tools from Software Verification

Martin Mariusz Lester

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## Solving XCSP3 constraint problems using tools from software verification

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Department of Computer Science, University of Reading, United Kingdom

#### Abstract

Since 2012, the Software Verification Competition (SV-COMP) has evaluated the performance of a wide range of software verification tools, using a wide range of automated reasoning techniques and representations. These include SAT/SMT solving, abstract interpretation, Constrained Horn Clause solving, numerical interval analysis and finite state automata.

The XCSP3 constraint format serves as an intermediate language for constraint problems. When trying to solve a particular problem, one often wishes to trial a range of techniques to see which one is initially most promising. Unfortunately, there might not always be an implementation of the desired technique for one's formulation.

We propose to address this problem by encoding XCSP3 problems as C program software verification problems. This grants the ability to trial all techniques implemented in SV-COMP entries. We should not expect the encoding to be efficient, but it may be sufficient to identify the most promising techniques.

2012 ACM Subject Classification Software and its engineering  $\rightarrow$  Software verification; Software and its engineering  $\rightarrow$  Constraint and logic languages

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## 1 Motivation

A popular constraint programming language is MiniZinc [6], which can be compiled down to the lower level format FlatZinc. It is easier for someone wanting to develop a new solver to target FlatZinc rather than MiniZinc. However, some of the structure of a constraint problem is usually lost in the translation, which is disadvantageous for some solver techniques. The XML-based intermediate representation for constraint problems XCSP3 [1] aims to fill the gap between these two extremes, providing a format that is easier than MiniZinc for a solver developer to target, while retaining more of a problem's structure than FlatZinc.

It is often the case that certain families of constraint problems are more amenable to solution using a particular method, so a user wishing to solve a particular problem may want to test several different solvers that implement complementary techniques.

Sometimes, one may wish to test a technique that comes from another research community, but which is not implemented in any constraint solver. The software verification community has developed a wide range of varied techniques for reasoning about execution of programs; these are evaluated in solvers submitted to the yearly Software Verification Competition (SV-COMP) [2]. In order to allow easy testing of these techniques on constraint problems, we propose to translate XCSP3 instances into C program verification problems.

A common form of program verification tool aims to prove that no execution of a program (with any variable values, not just those specified in tests) can lead to an assertion violation. If an assertion violation is possible, it gives a trace of execution of the program, showing how the assertion violation occurs and the values of variables along that trace. We can repurpose this to solve constraint problems by constructing a program in which an assertion violation occurs only when its variables' values satisfy a constraint problem. Then the counterexample trace produced by the verification tool will give the solution to the constraint problem.

## 2 Example

To demonstrate our idea, we consider a simple example from the XCSP3-core specification [3]:

**Listing 1** A simple XCSP3 example: find integers  $1 \le x \le 10$  and  $1 \le y \le 100$  with  $y = x^2$ .

**Listing 2** A reformulation in C; functions beginning \_\_\_\_VERIFIER are standard in SV-COMP.

```
int main() {
    uint32_t x;
    uint32_t y;

    x = __VERIFIER_nondet_u32();
    __VERIFIER_assume(x >= 1 && x <= 10);
    y = __VERIFIER_nondet_u32();
    __VERIFIER_assume(y >= 1 && y <= 100);

    __VERIFIER_assume(y == x*x);

    __VERIFIER_error();
}</pre>
```

In our C translation of the problem, the XCSP3 variables x and y are mapped directly onto C variables with the same names. In order to express that the values of these variables are not fixed, we use \_\_VERIFIER\_nondet\_u32() to assign a value nondeterministically. To express that the variables have a limited range, we use \_\_VERIFIER\_assume() to tell the verifier to *ignore* paths through the program that *do not* meet the specified condition.

The intension constraint is also translated into an assume statement, with the content of the constraint converted to C syntax.

Finally, the line \_\_VERIFIER\_error(), similar to the C idiom assert(0), indicates that it is an error for execution of the program to complete (if all assumptions are true).

### 3 Status

We have developed a prototype of our transformation tool, xcsp2c. In combination with the bounded model-checking program verification tool CBMC [4], which uses a translation to SAT, this yields a solver called *Exchequer*, which we have entered into the XCSP Competition 2022 mini solver tracks. Our hypothesis is that this combination will perform acceptably, but worse than the direct approach used by PicatSAT [7], which also uses a translation to SAT, and ranked highly in the the XCSP Competition 2018 [5] and 2019. We are investigating whether we can improve *Exchequer* by using other verification tools from SV-COMP.

M. M. Lester

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